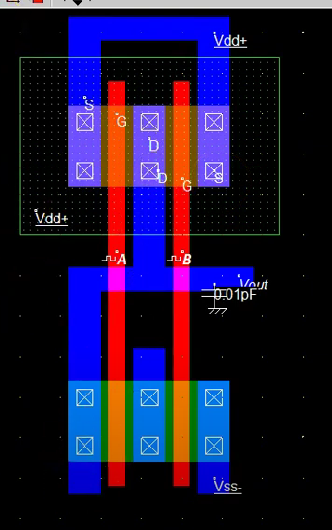
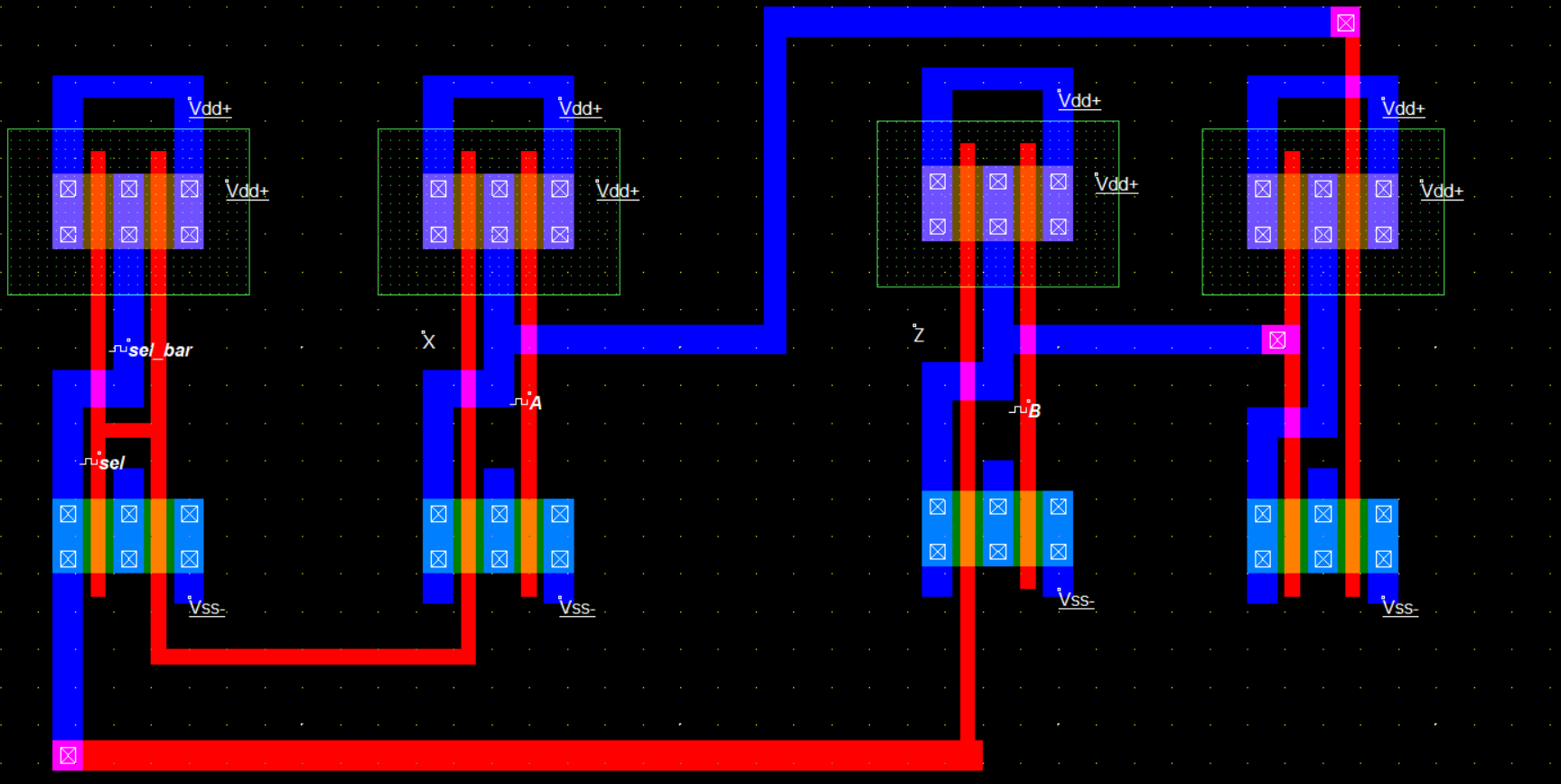


NOR

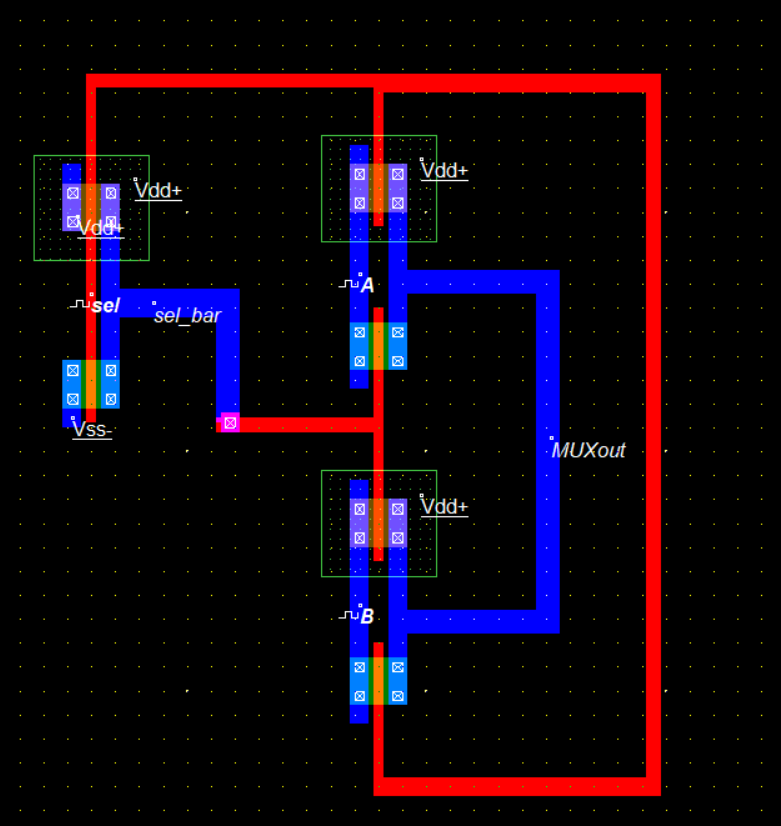
Nand



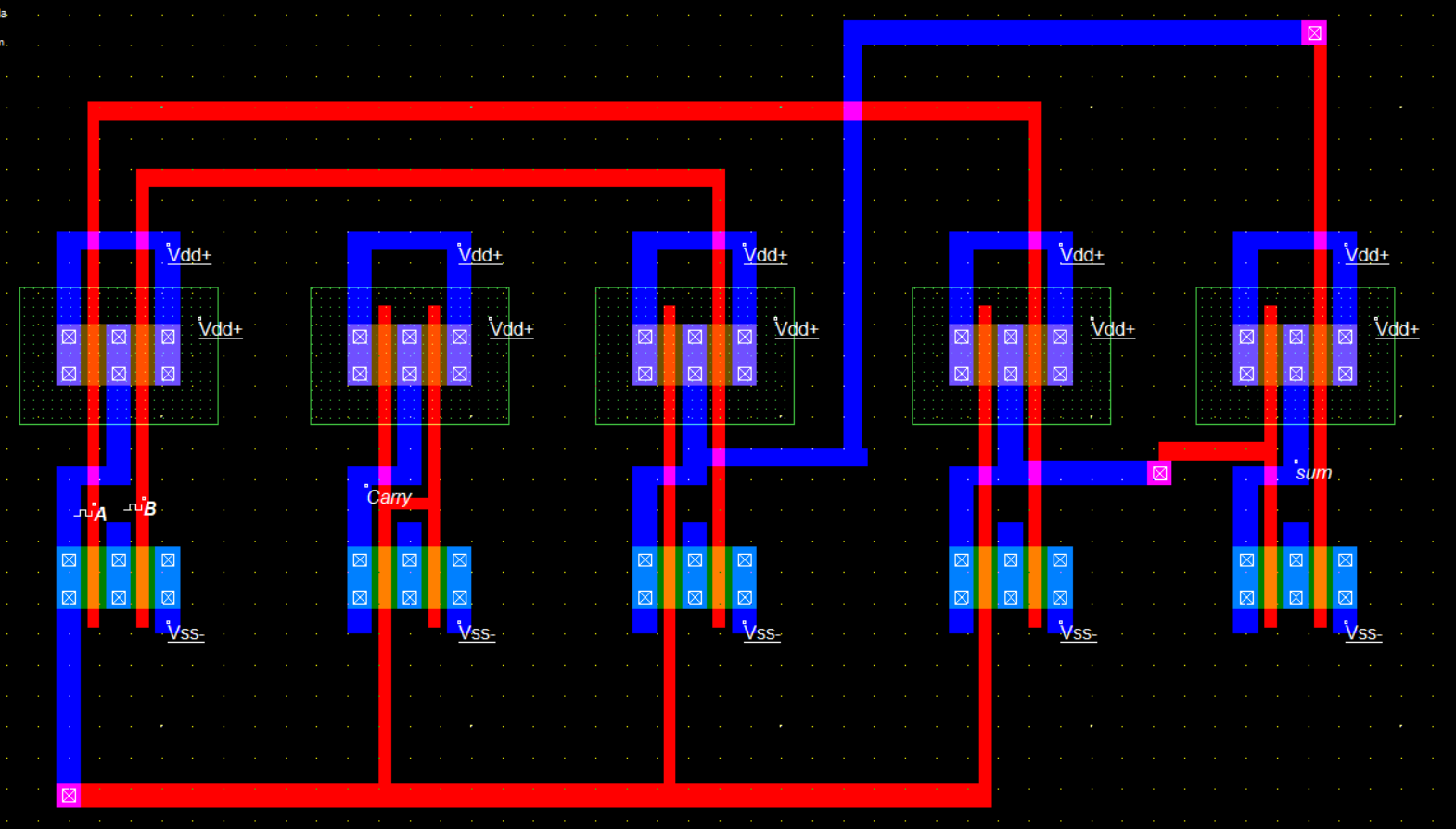
2:1 MUX using NAND gate



2:1 MUX using transmission gate



Half adder using NAND Gate



Half adder using transmission gate

